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Dzi	AR	1	MIPS IV Instru	ction Set (R10000), Revision 3.2, MIPS Technologies,	Inc., entire publica	tion submitte	ed (September,	1995).
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Page 2 of 6 ATTY, DOCKET NO. APPLICATION NO. 09/364,786 0077.20 (1778.0120002) **FORM PTO-1449 APPLICANT** Thekkath et al. INFORMATION DISCLOSURE STATEMENT **FILING DATE GROUP** July 30, 1999 2672 **U.S. PATENT DOCUMENTS EXAMINER CLASS** SUB-CLASS INITIAL DOCUMENT DATE NAME FILING DATE NUMBER AA2 AB2 AC2 AD2 AE2 Technology Center 2600 AF2 AG2 AH2 AI2 AJ2 AK2 FOREIGN PATENT DOCUMENTS **EXAMINER** INITIAL DOCUMENT DATE COUNTRY CLASS SUB-CLASS **TRANSLATION** NUMBER Yes AL2 No Yes AM2 No Yes AN2 No Yes No AO2 Yes AP2 No OTHER (Including Author, Title, Date, Pertinent Pages, etc.) Kubosawa, H. et al., "Four-way VLIW Geometry Processor for 3D Graphics Applications," FUJITSU Sci. Tech. J., 36, 1, pp.39-47 (June AR 2 2000) (manuscript received December 1, 1999). DIC Thekkath et al., "An Architecture Extension for Efficient Geometry Processing," slides 1-23, presented at Hot Chips 11, A Symposium DIL AS 2 of High-Performance Chips, Stanford Univ. (August 1999) (submitted for conference review July 14, 1999). Uhler, M., "Optimizing Game Applications for the MIPS RISC Architecture," Computer Game Developer's Conference, San Jose, CA, 14 pages (March 1999) (submitted for conference review on 2/12/99). ΑT 2 171 **EXAMINER** DATE CONSIDERED Danrl

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b7C	АТ	3	AltiVec™ Te	echnology Programr	ning Environments Manual, Rev. 0.1, N	Notorola, Inc., entire docume	ent submitted (Nov	vember 1998).		

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